

CHANNEL SELECTING DEVICE

CHANNEL SELECTING DEVICE

Patent Number: JP58097917
Publication date: 1983-06-10
Inventor(s): OZAKI HIROYUKI
Applicant(s): MATSUSHITA DENKI SANGYO KK
Requested Patent: ☐ JP58097917
Application Number: JP19810197527 19811207
Priority Number(s):
IPC Classification: H03J7/02
EC Classification:
EC Classification:
Equivalents:

Abstract

PURPOSE: To perform invariably accurate AFC operation without reference to whether a tuning voltage is high or low by applying the AFC voltage of an AFC detecting circuit to an emitter follower circuit and then applying it to a synthesizing resistance network.

CONSTITUTION: The AFC voltage of an AFC detecting circuit 5 is applied to the base of a transistor (TR) 7 constituting an emitter follower circuit 6 and an output is led out of the emitter and applied to a synthesizing resistance network 8; and it is synthesized with a tuning voltage at a prescribed ratio, and the resulting voltage is applied to the varactor diode 2 of an electronic tuner 1. Consequently, the AFC wave detecting circuit 5 is separated from the network 8 and the center voltage of the AFC voltage is held stably at the constant level obtained by adding the base-emitter voltage of the TR7 to the bias voltage depending upon the voltage division ratio of resistances 9 and 10.